STUDY MODULE DESCRIPTION FORM							
Name of the module/subject Concurrent programming						Code 1010334451010335200	
Field of study				Profile of study		Year /Semester	
Information Engineering				(general academic, practical) (brak) 3 / 5		3 / 5	
Elective path/specialty				Subject offered in: polish			
Cycle of study:				Form of study (full-time,part-time)			
First-cycle studies				part-time			
No. of hours				No. of credits			
Lecture:	16 Classes	s: - Laboratory: 12		Project/seminars:	-	4	
Status of the course in the study program (Basic, major, other) (university-wide, from another							
(brak)					(brak)		
Education areas and fields of science and art						ECTS distribution (number and %)	
technical sciences						4 100%	
email: andrzej.sikorski@put.poznan.pl tel. +48(61)6653730 Electrical Engineering ul. Piotrowo 3A 60-965 Poznań Prerequisites in terms of knowledge, skills and social competencies:							
1 K ı	nowledge	Basic knowledge of numeric alg Basics of calculus, algebra and	e of numeric algorithms and combinatorics us, algebra and set theory.				
	-111-	Proficiency in any OOP language.					
2 SI	Skills Win32 or .NET programming						
5	ocial ompetencies	students are expected to be quiet during the lecture					
Assumptions and objectives of the course:							
Proficiency in concurrent programming. Designing of synchronization schemes. Ability to efficiently and correctly identify possible application of available standard design paterns.							
Study outcomes and reference to the educational results for a field of study							
Knowledge:							
Skills:							
Social competencies:							

Assessment methods of study outcomes

examination, laboratory reports, projects.

Course description

Sequential optimization. Superscalar optimization. Machine code level optimizations recommended by AMD for FPU operations. Declarative concurrency supported by OpenMP.

Operating System level facilities for concurrent/parallel programming. Constructs and techniques available on win32, .net,java. Intel TBB dynamic parallelism optimally exploiting the harware and computational state (cache buffering, data availability and internal redundance of processing components)

Basic bibliography:

M.Herlihy N.Shavit : The Art of Multiprocessor Programming Publisher: Morgan Kaufmann; 1 edition (March 14, 2008)
 M. Ben-Ari Principles of Concurrent and Distributed Programming Addison-Wesley; 2 edition (March 6, 2006)

Additional bibliography: Result of average student's workload Time (working Activity hours) Student's workload Source of workload hours ECTS Total workload 100 4 Contact hours 50 2 50 2 Practical activities